

### **ABSTRACT OF THE DISCLOSURE**

1       A noise level detecting circuit that can stably detect a noise with respect to a video  
2       signal or the like on which a copy guard signal is superimposed is disclosed. A noise level  
3       detecting circuit (100) of one embodiment can include an A/D converter (1) that converts an  
4       analog video signal (S1) into a digital video signal (S4). A timing generating circuit (3) may  
5       generate a burst gate pulse (S3) that determines a period during which the burst signal of the  
6       analog video signal (S1) is extracted. An absolute value circuit (5) may convert an extracted  
7       burst signal (S6) into only a positive pole component on the basis of a pedestal level signal  
8       (S5). A delay circuit (6) may delay the burst signal (S7) of only the positive pole component  
9       by 1H unit. A comparator (7) may compare the burst signal (S7) of only the positive pole  
10      component with the delayed burst signal (S8). In this way, the noise may be stably detected.